

ABSTRACT:

The invention relates to a device which comprises a first and a second processor module. The second module M2 is intended to receive data and instructions and to execute operations for obtaining a result. The first module is intended to transmit instructions to the second module according to a predetermined scheme, each instruction indicating the operation it is provided to execute in the current time slot.

According to the invention, an operation is only executed by the second module if the necessary data are available. And it is not possible to execute a different operation from the one that is provided in the current time slot. Thus, no result can be delivered outside the time slots provided for this purpose.

Application: any type of circuit requiring a static and nevertheless programmable architecture; notably programmable digital demodulators.

Fig. 2